

FIG.1A

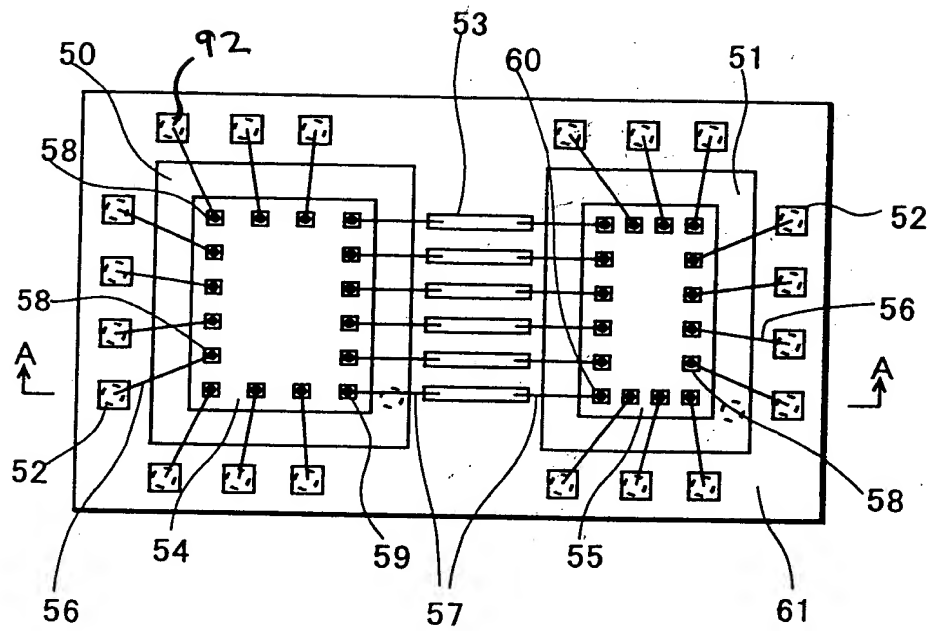


FIG.1B

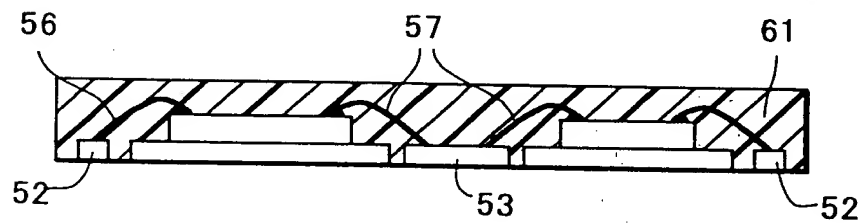


FIG.2A

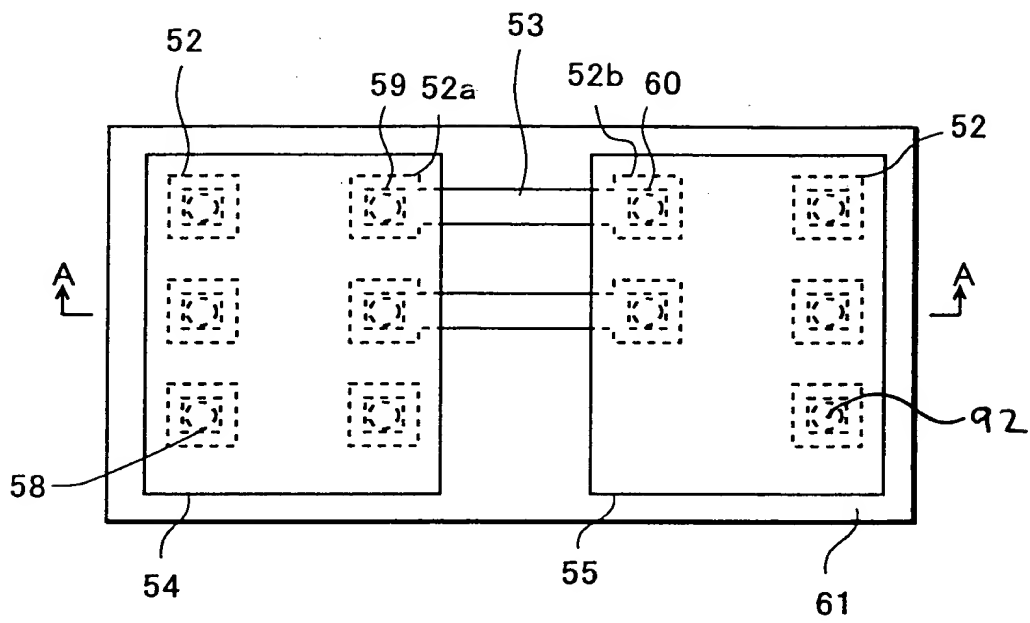


FIG.2B

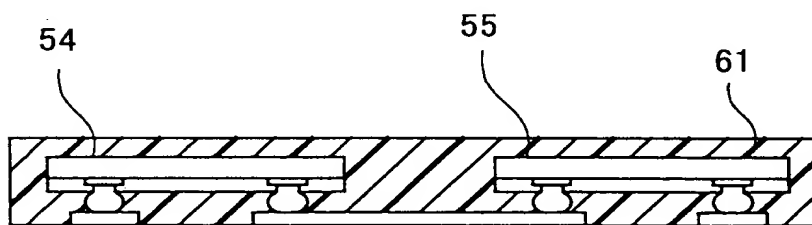


FIG.3A

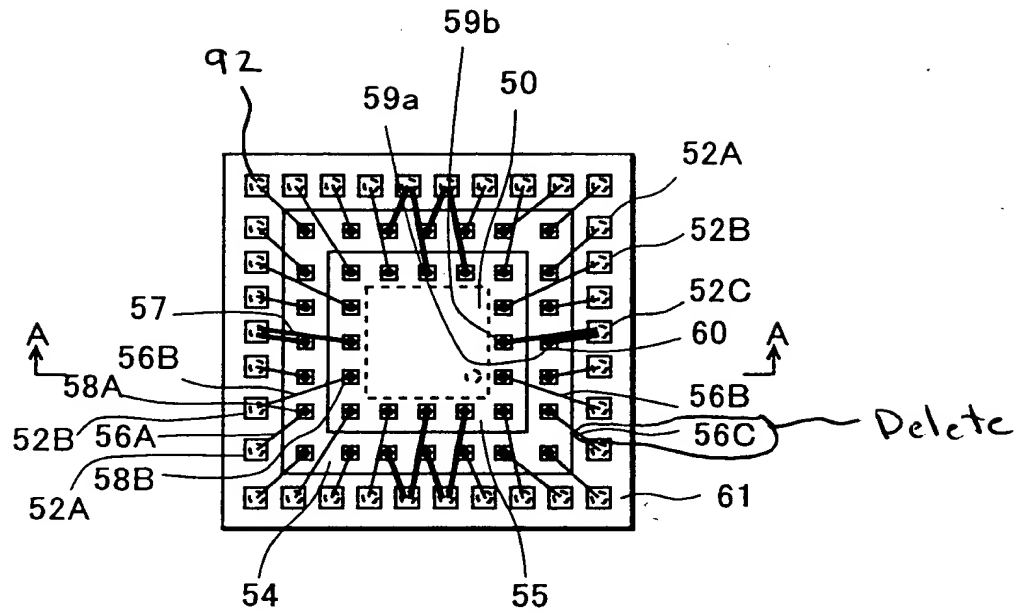
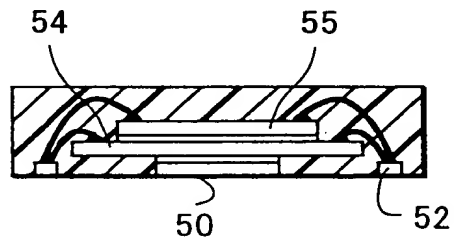


FIG.3B



The diagram shows a rectangular device with a central rectangular area defined by a dashed line. This central area contains several horizontal rectangular blocks. Surrounding this central area is a grid of small squares, each containing a small circle. The device is labeled with various numbers and letters: 50, 51, 52, 53, 54, 55, 56A, 56B, 57, 58, 59A, 59B, 60A, 61, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 112, 113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127, 128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140, 141, 142, 143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153, 154, 155, 156, 157, 158, 159, 160, 161, 162, 163, 164, 165, 166, 167, 168, 169, 170, 171, 172, 173, 174, 175, 176, 177, 178, 179, 180, 181, 182, 183, 184, 185, 186, 187, 188, 189, 190, 191, 192, 193, 194, 195, 196, 197, 198, 199, 200. Handwritten annotations include 'Delete' and 'A' with arrows.

A cross-sectional view of a substrate 61. A conductive layer 62 is formed on the top surface of the substrate 61. A wiring pattern 63 is formed on the conductive layer 62, consisting of a central horizontal line and two curved lines extending from its ends.

FIG.5

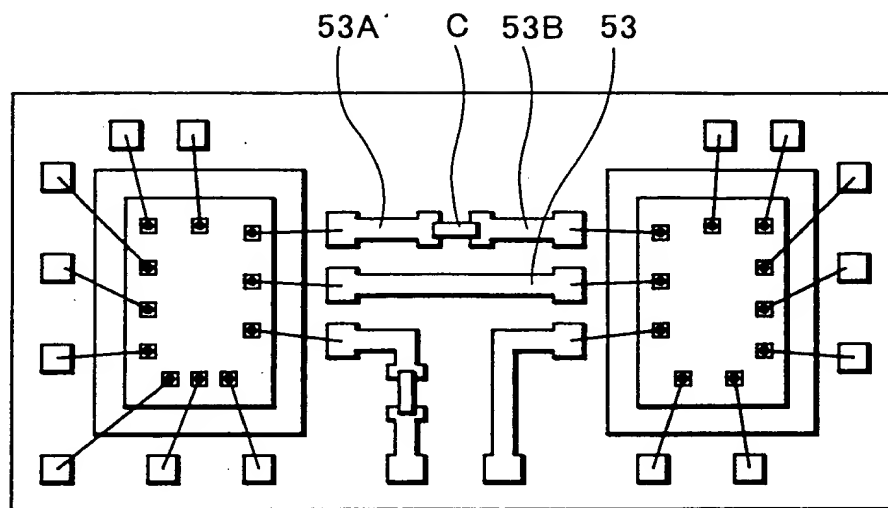


FIG.6

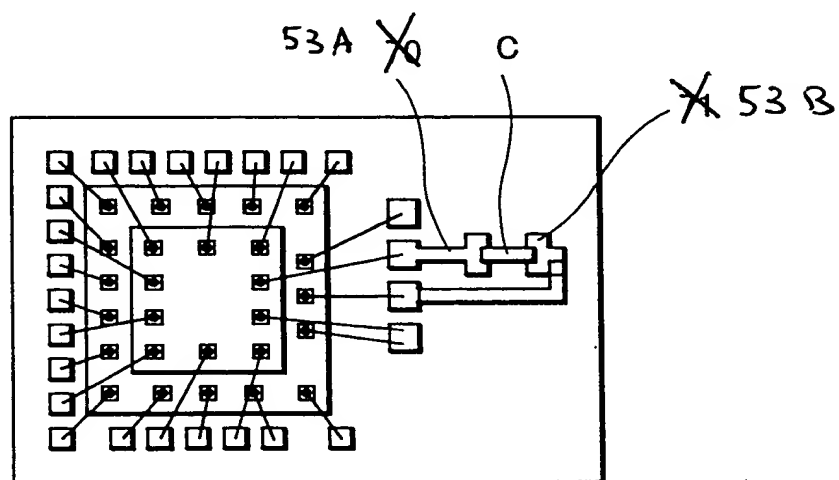


FIG.7

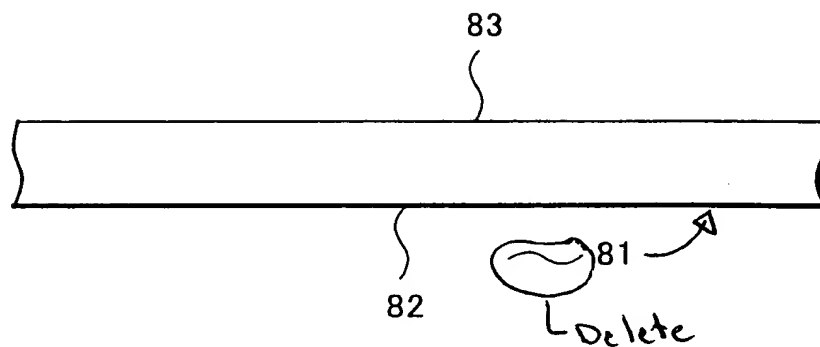


FIG.8

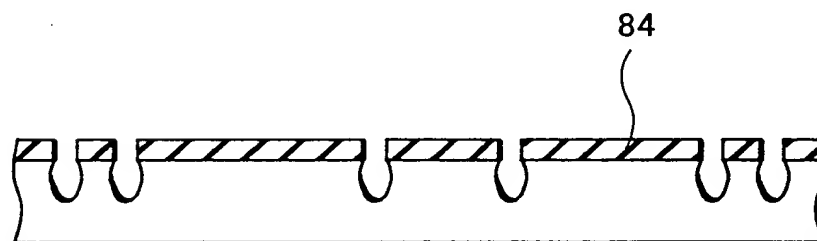


FIG.9

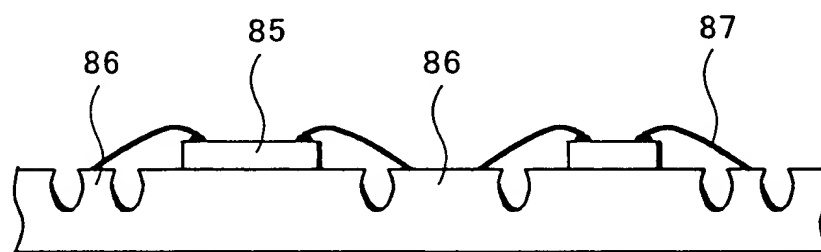


FIG.10

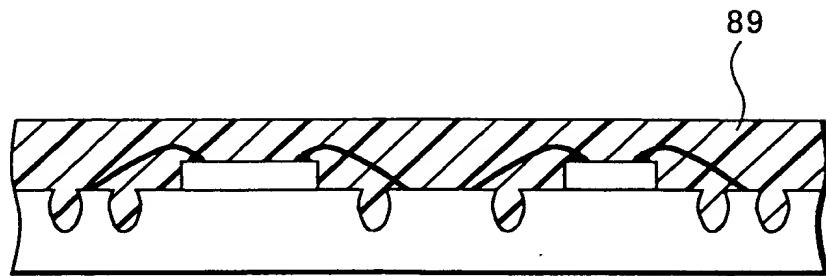


FIG.11

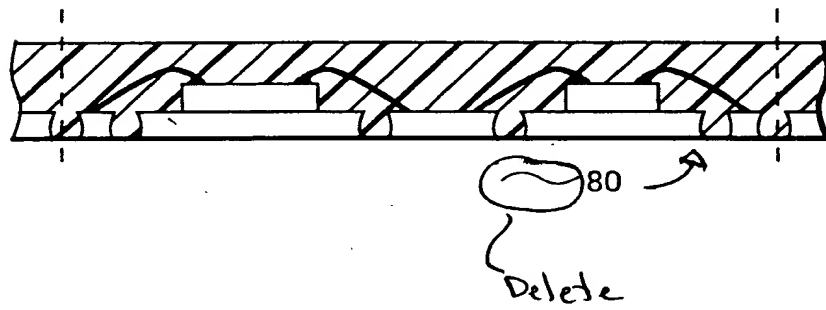


FIG.12 (Prior Art)

